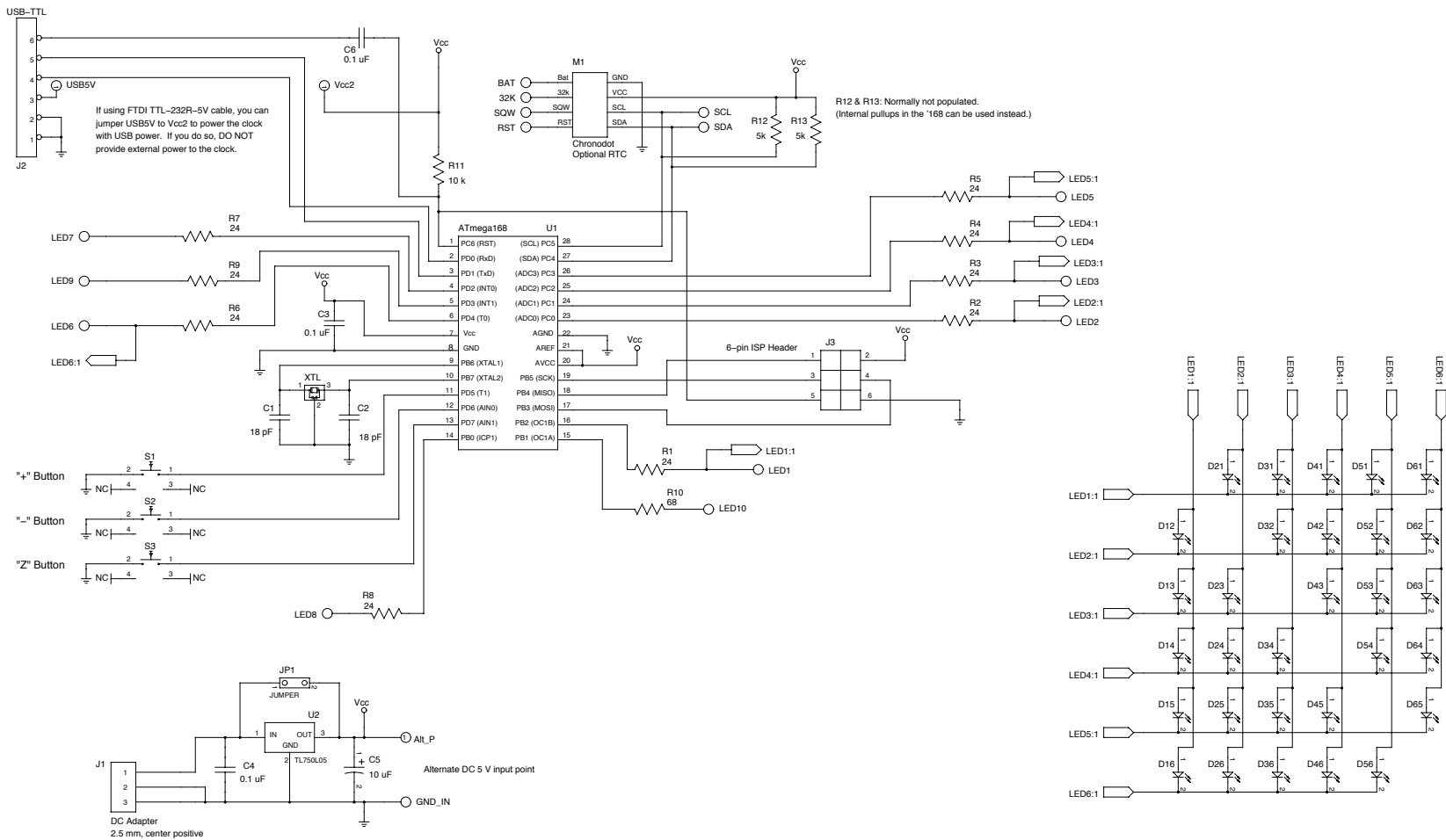


Connector for FTDI USB-TTL cable.



Regulated 5 V DC adapter is normally used,
along with jumper in location JP1.

For unregulated input above 5.5 V, remove jumper JP1
and install a TL750L05 regulator plus two caps as shown.

An Open-Source Design by
Evil Mad Scientist Laboratories
www.evilmadscientist.com

Fabricated for
Evil Mad Science LLC
<http://evilmadscience.com/>

Copyright 2009, Windell H. Oskay/Evil Mad Science LLC
Distributed under the TAPR Open Hardware License
www.tapr.org/OHL

Please see "copying.txt" for important information
about using this reference design.

Bulbdial Clock: Main PCB

TITLE		REVISION: A	
FILE:	bdmain.sch	DRAWN BY: Windell H. Oskay	
PAGE:	1 OF 1		