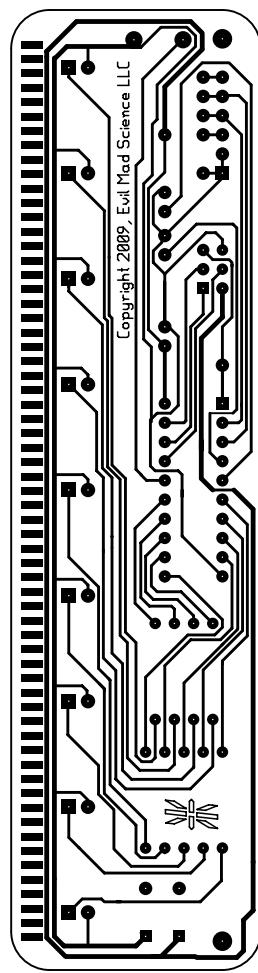
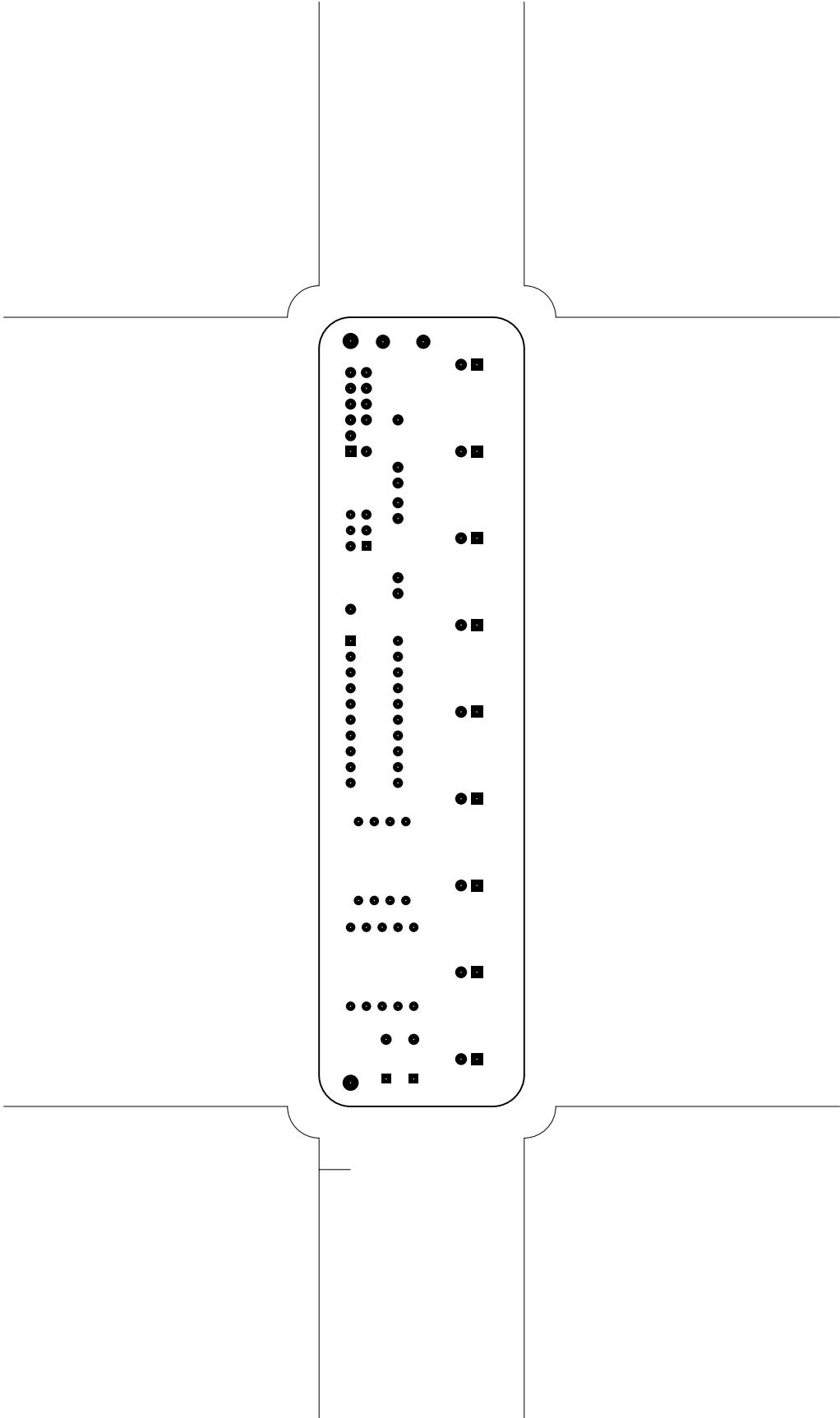


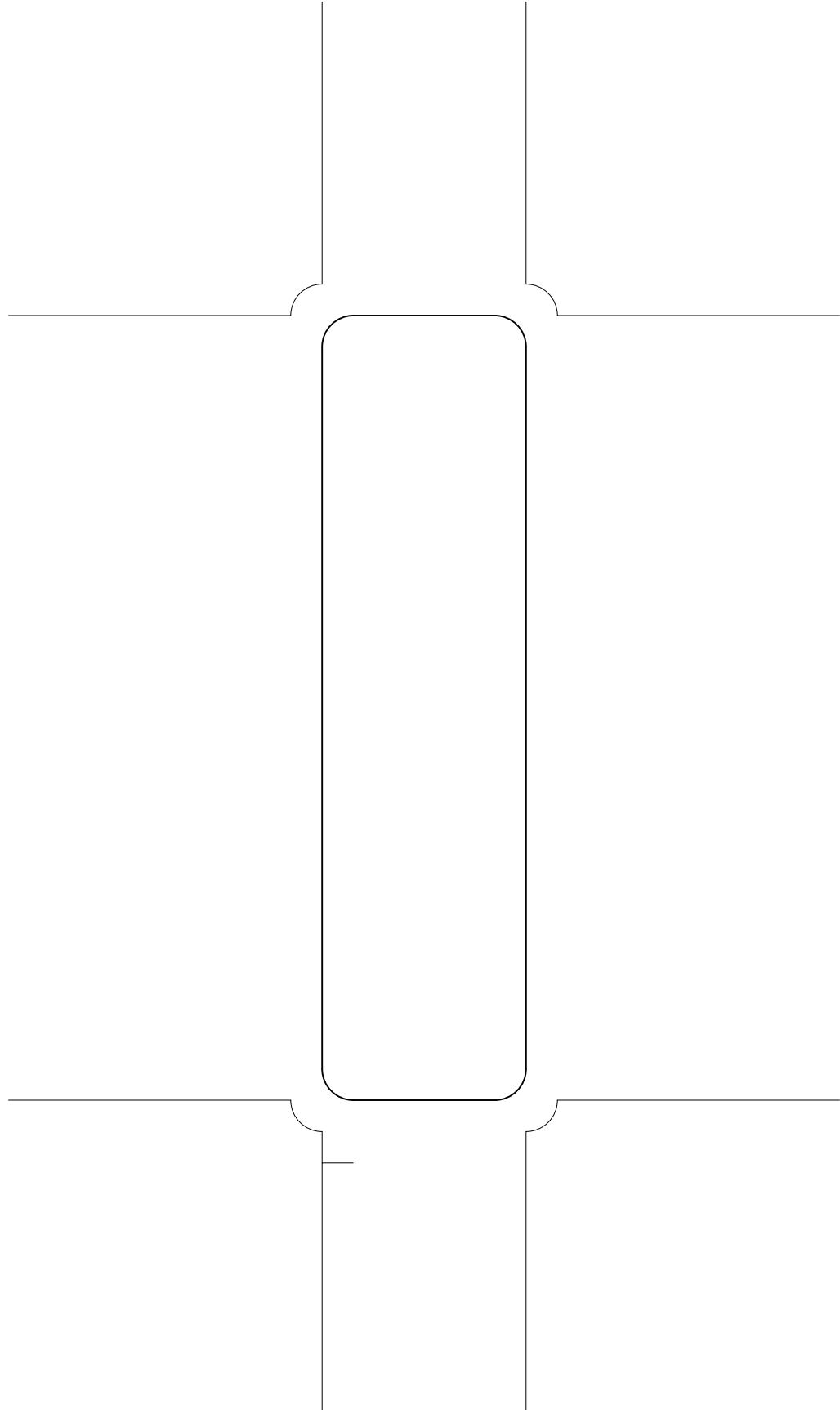
2. solder
3. component
4. outline
5. componentmask
6. soldermask
7. plated-drill
8. topsilk
9. bottomsilk
10. topassembly
11. bottomassembly
12. fab



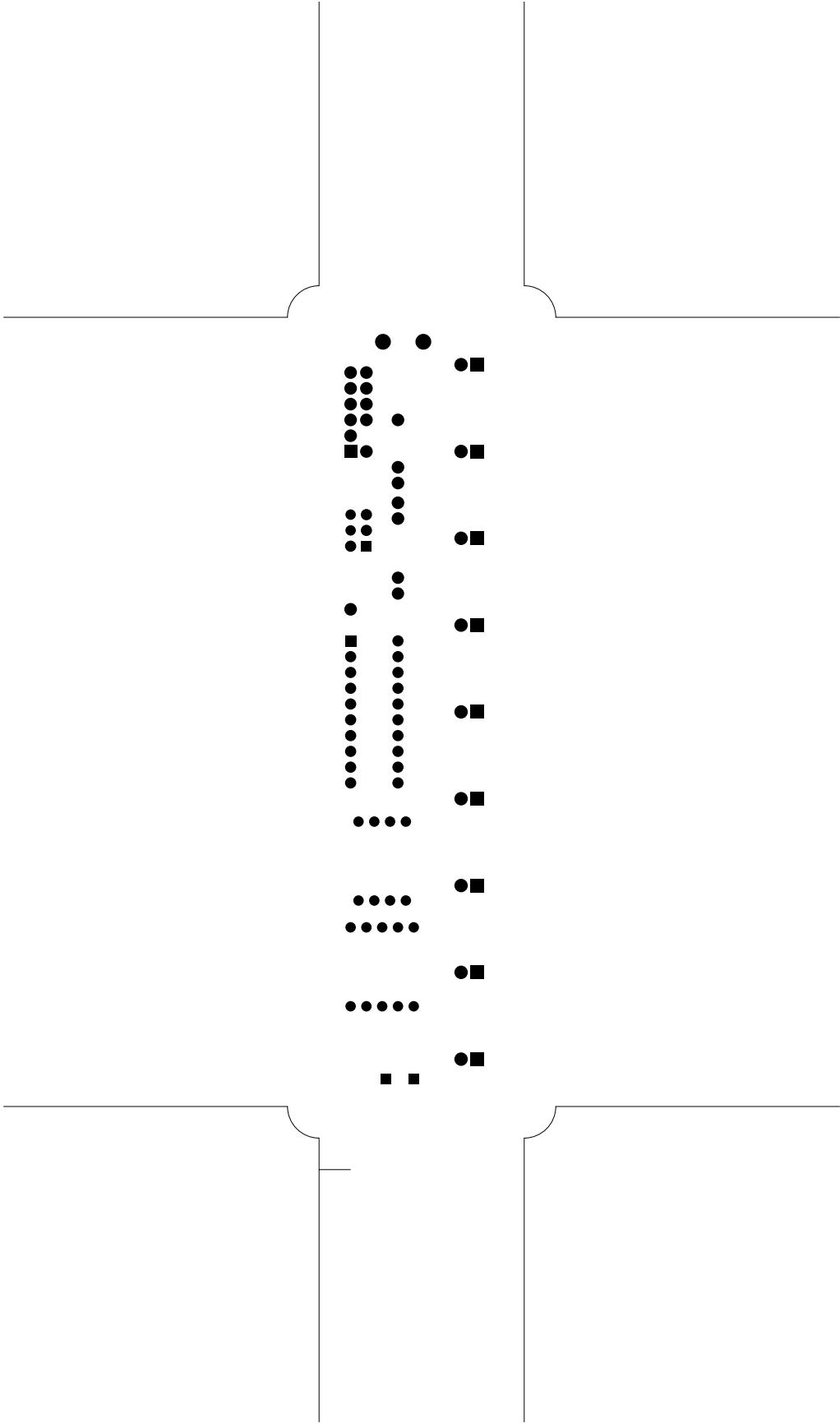
ix_revC, back (mirrored), scale = 1:1.000
/Users/oskay/Documents/UnixSoftware/gedafiles/ix/ix_RevC.pcb



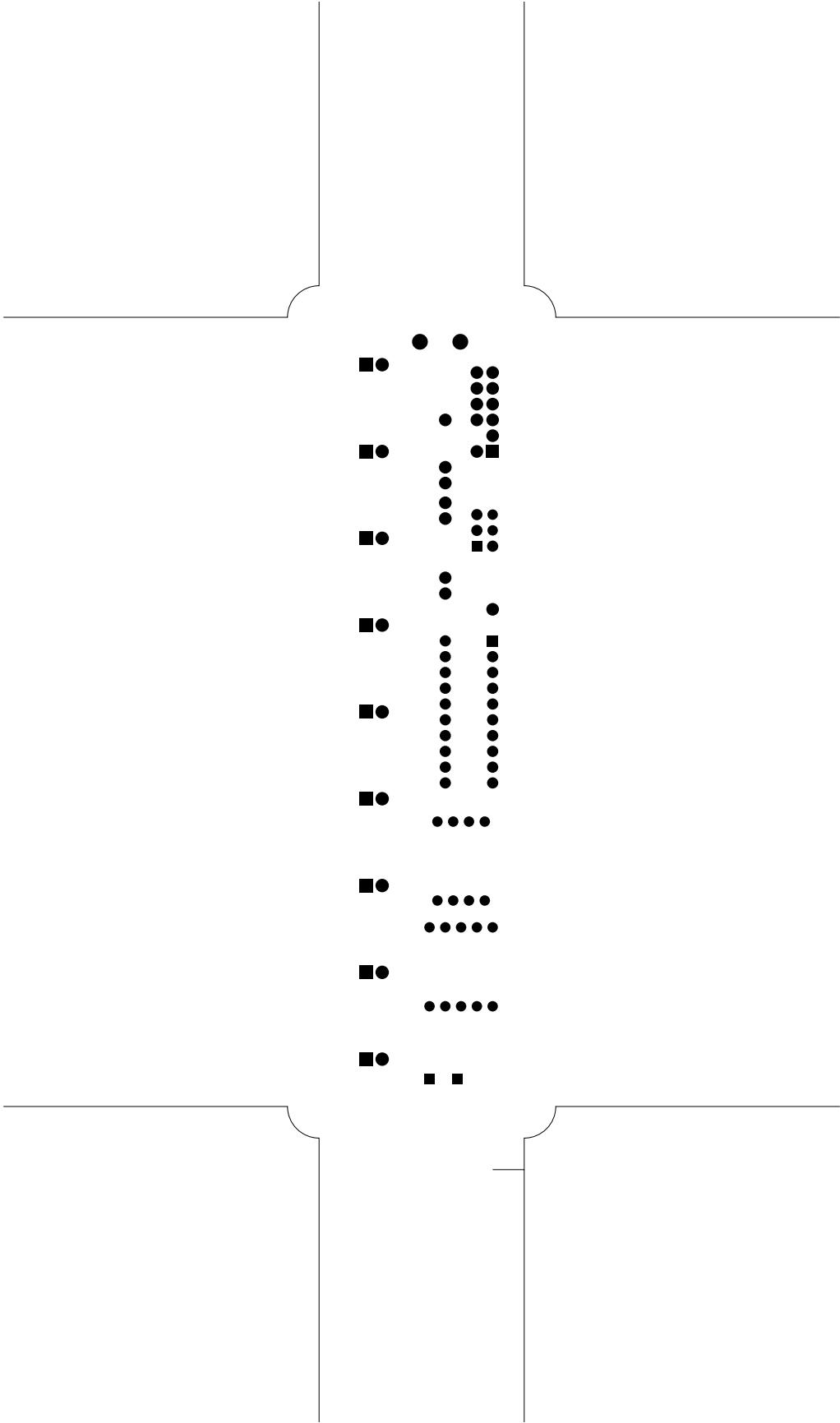
ix_revC, front, scale = 1:1.000
/Users/oskay/Documents/UnixSoftware/gedafiles/ix/ix_RevC.pcb



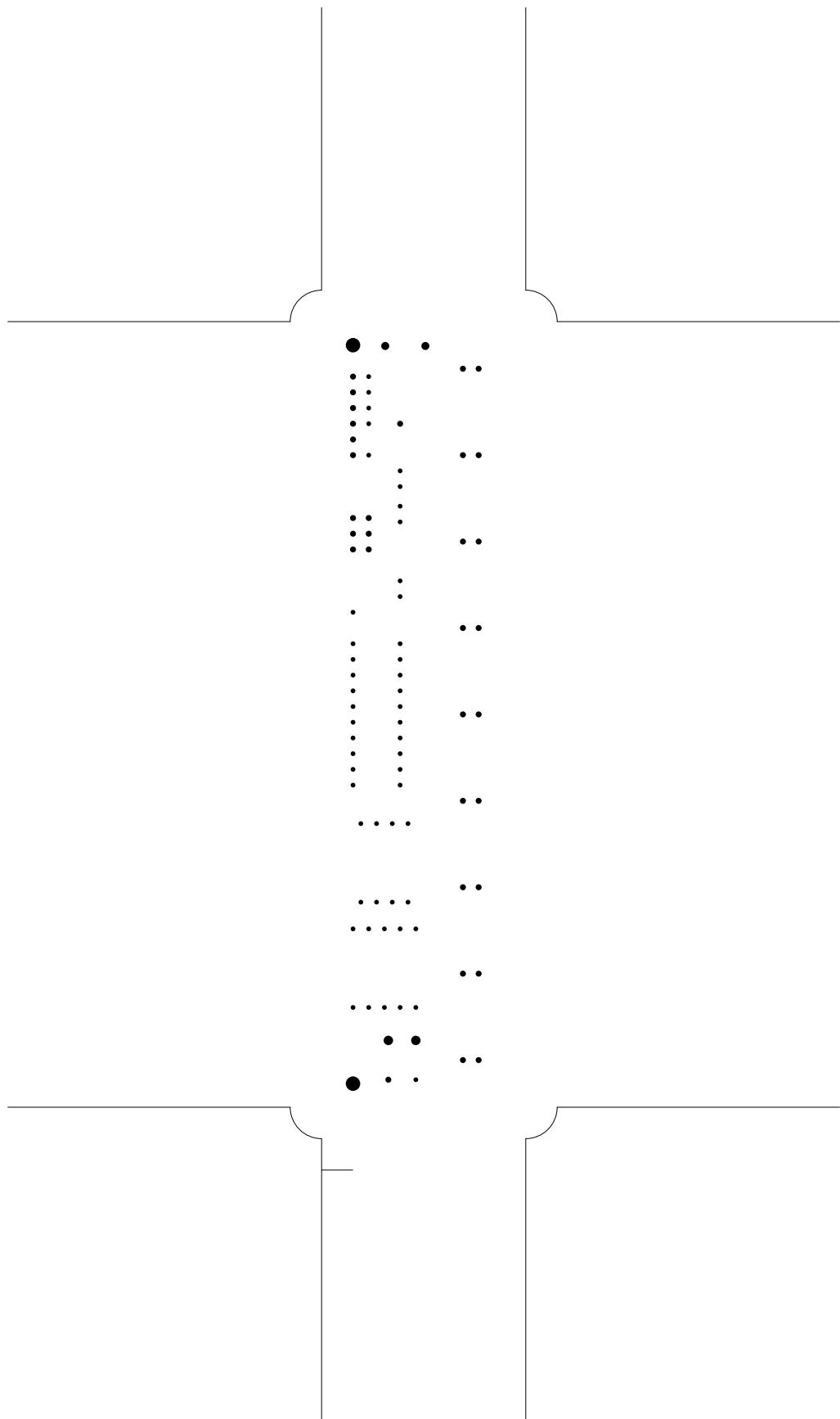
ix_revC, outline, scale = 1:1.000
/Users/oskay/Documents/UnixSoftware/gedafiles/ix/ix_RevC.pcb



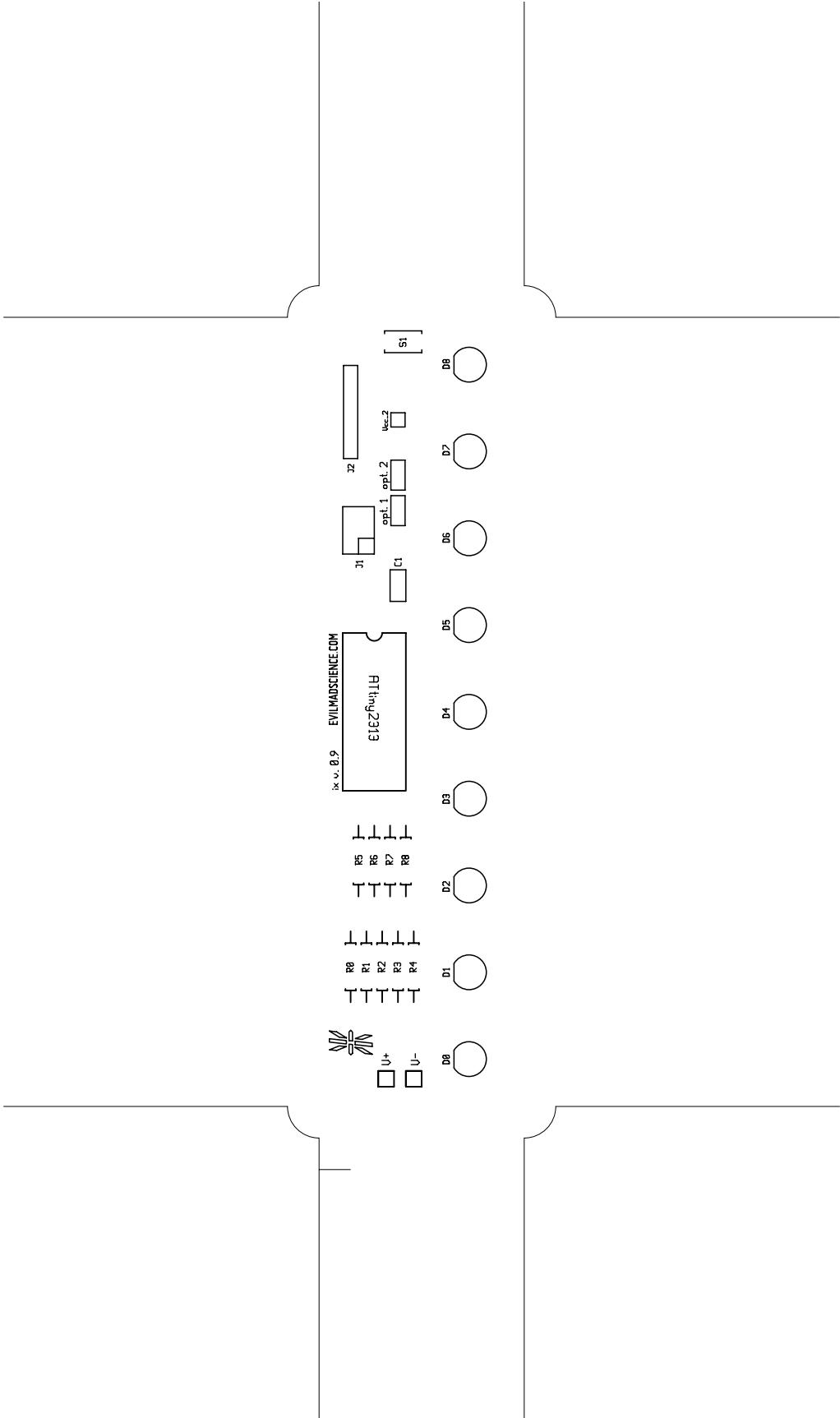
ix_revC, frontmask, scale = 1:1.000
/Users/oskay/Documents/UnixSoftware/gedafiles/ix/ix_RevC.pcb



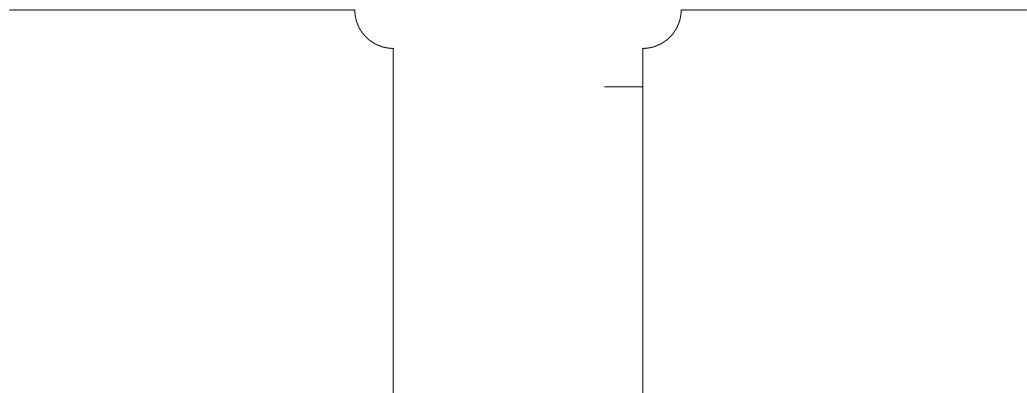
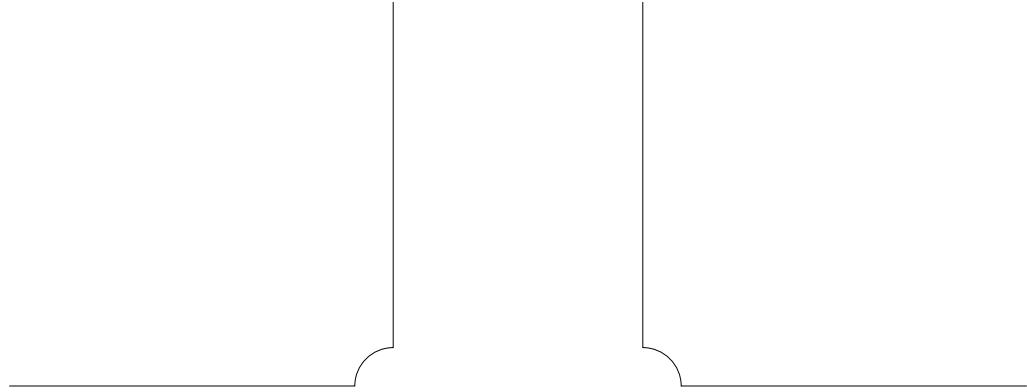
ix_revC, backmask (mirrored), scale = 1:1.000
/Users/oskay/Documents/UnixSoftware/gedafiles/ix/ix_RevC.pcb



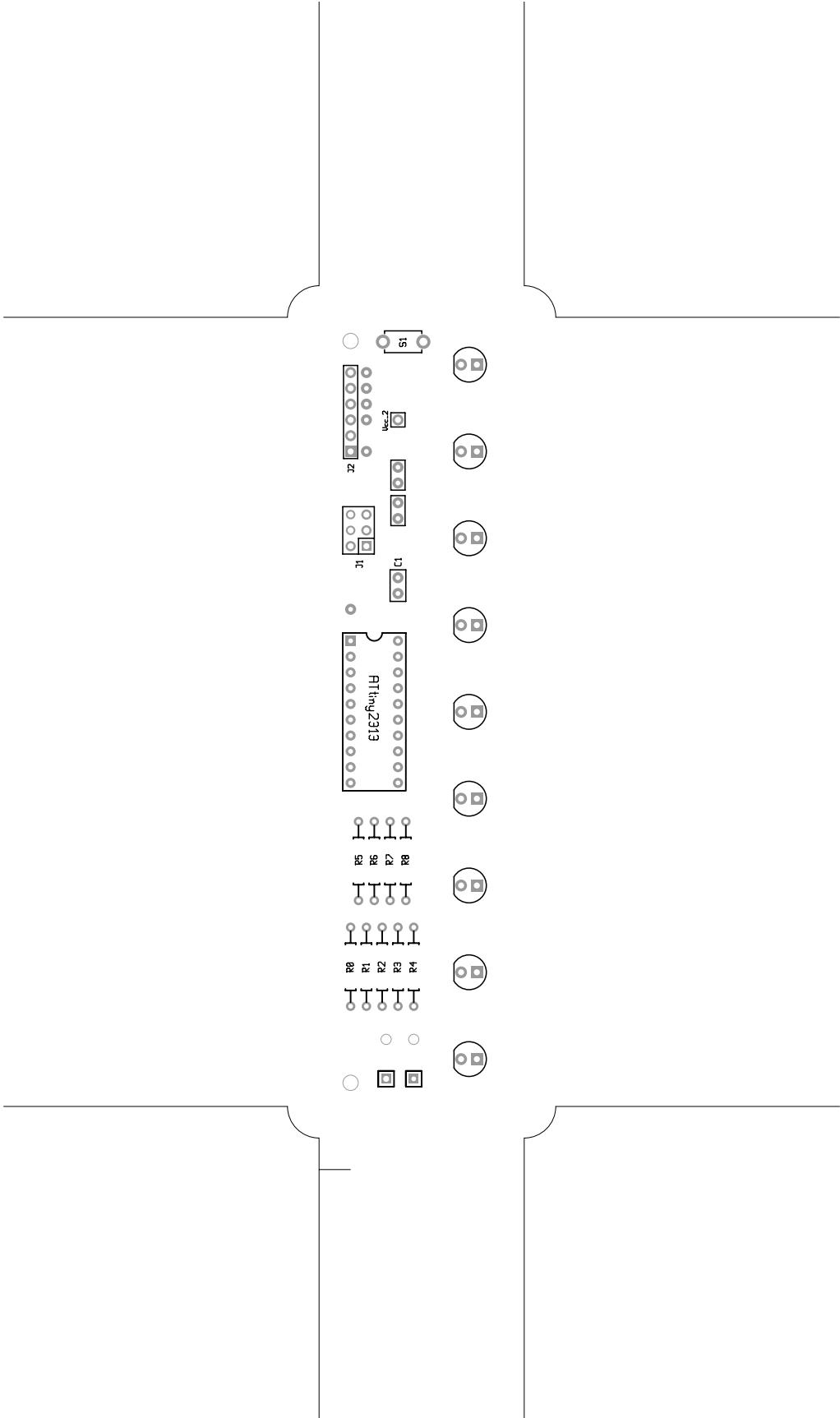
ix_revC, plated-drill, scale = 1:1.000
/Users/oskay/Documents/UnixSoftware/gedafiles/ix/ix_RevC.pcb



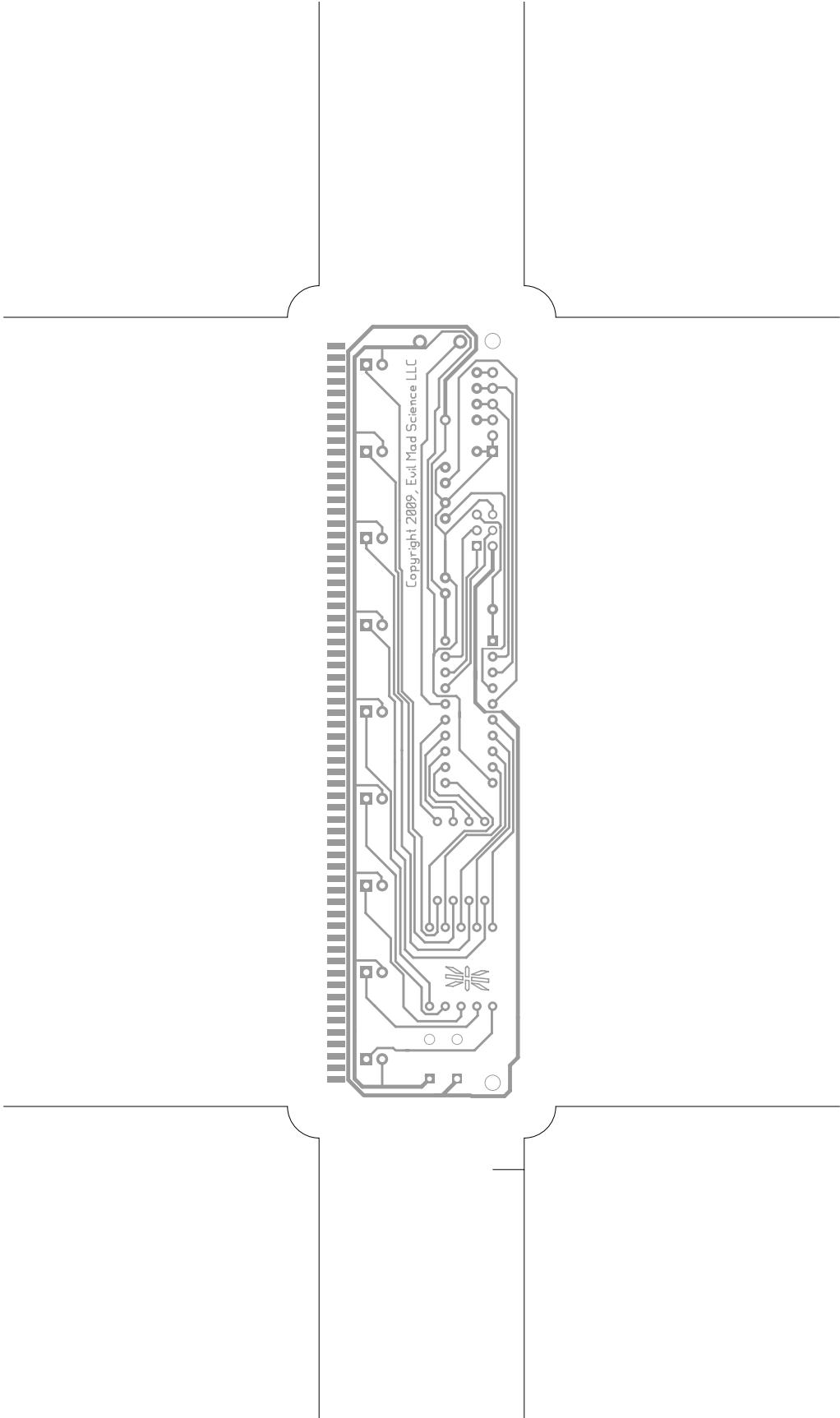
ix_revC, frontsilk, scale = 1:1.000
/Users/oskay/Documents/UnixSoftware/gedafiles/ix/ix_RevC.pcb



ix_revC, backsilk (mirrored), scale = 1:1.000
/Users/oskay/Documents/UnixSoftware/gedafiles/ix/ix_RevC.pcb



ix_revC, frontassembly, scale = 1:1.000
/Users/oskay/Documents/UnixSoftware/gedafiles/ix/ix_RevC.pcb



ix_revC, backassembly (mirrored), scale = 1:1.000
/Users/oskay/Documents/UnixSoftware/gedafiles/ix/ix_RevC.pcb

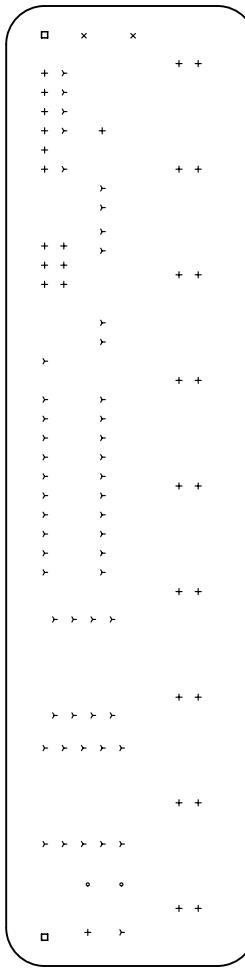
There are 5 different drill sizes used in this layout, 89 holes total

Symbol	Diam. [inch]	Count	Plated?
x	0.038	51	YES
+	0.038	32	YES
x	0.051	2	YES
o	0.060	2	YES
□	0.092	2	YES

Title: ix_revC - Fabrication Drawing

Author: Kindell H. Oskay

Date: Wed Sep 30 11:27:00 2009 UTC



Board outline is the centerline of this path